U.S. patent application serial number 10/045,923 by W. A. Downer et al. (BEA920000021US1) entitled "Hardware Support For Partitioning A Multiprocessor System To Allow Distinct Operating Systems" was filed on January 9, 2002.

U.S. patent application serial number 10/045,925 by T. B. Berg et al. (BEA920000022US1) entitled "Distributed Allocation Of System Hardware Resources For Multiprocessor Systems" was filed on January 9, 2002.

U.S. patent application serial number 10/045,926 by W. A. Downer et al. (BEA920010030US1) entitled "Masterless Building Block Binding To Partitions" was filed on January 9, 2002.

U.S. patent application serial number 10/045,774 by W. A. Downer et al. (BEA920010031US1) entitled "Building Block Removal From Partitions" was filed on January 9, 2002.

U.S. patent application serial number 10/045,796 by W. A. Downer et al. (BEA920010041US1) entitled "Masterless Building Block Binding To Partitions Using Identifiers And Indicators" was filed on January 9, 2002.

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REMARKS .

This Preliminary Amendment is made to update the Specification's "Cross-Reference to Related Applications" with the application numbers and filing dates of the applications listed therein.

Applicants submit that this application is now in condition for examination, and respectfully request early and favorable consideration. The Examiner is urged to call the undersigned at the below-listed telephone number if, in the Examiner's opinion, such a phone conference would expedite or aid in the search or examination of this application.

Respectfully submitted,

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Docket No.:   BEA9-1999-0003-US1   3   Preliminary Amendment
Serial No.:   10/045,798   June 13, 2002

ATTACHMENT A TO AMENDMENT

Marked-up Version Showing Changes Made by this Amendment

Changes made by this amendment are marked-up below, showing [deletions in brackets] and insertions underlined .

In the Specification

Page 1, line 7, through page 2, line 3, has been amended as follows.

U.S. patent application serial number [/ ,] 10/045,927 by T. B. Berg et al. (BEA920000017US1) entitled "Method And Apparatus For Using Global Snooping To Provide Cache Coherence To Distributed Computer Nodes In A Single Coherent System" was filed on [January __, 2002] January 9, 2002.

U.S. patent application serial number [/ ,] 10/045,564 by S.G. Lloyd et al. (BEA920000019US1) entitled "Transaction Redirection Mechanism For Handling Late Specification Changes And Design Errors" was filed on [January __, 2002] January 9, 2002.

U.S. patent application serial number [/ ,] 10/045,797 by T. B. Berg et al. (BEA920000020US1) entitled "Method And Apparatus For Multi-path Data Storage And Retrieval" was filed on [January __, 2002] January 9, 2002.

U.S. patent application serial number [/ ,] 10/045,923 by W. A. Downer et al. (BEA920000021US1) entitled "Hardware Support For Partitioning A Multiprocessor System To Allow Distinct Operating Systems" was filed on January __, 2002 January 9, 2002.

U.S. patent application serial number [/ ,] 10/045,925 by T. B. Berg et al. (BEA920000022US1) entitled "Distributed Allocation Of System Hardware Resources For Multiprocessor Systems" was filed on [January __, 2002] January 9, 2002.

U.S. patent application serial number [/ ,]10/045,926 by W. A. Downer et al. (BEA920010030US1) entitled "Masterless Building Block Binding To Partitions" was filed on [January __, 2002] January 9, 2002.

U.S. patent application serial number [/ ,] 10/045,774 by W. A. Downer et al. (BEA920010031US1) entitled "Building Block Removal From Partitions" was filed on [January __, 2002] January 9, 2002.

Docket No.:   BEA9-1999-0003-US1   4   Preliminary Amendment
Serial No.:   10/045,798   June 13, 2002

U.S. patent application serial number [___/___,___] 10/045,796 by W. A. Downer et al. (BEA920010041US1) entitled "Masterless Building Block Binding To Partitions Using Identifiers And Indicators" was filed on [January __, 2002] January 9, 2002.

The transmission ends with this page